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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Trammel, et al.

Serial No. : 09/940,567

Examiner: Russel, V.

Filed : 8/27/01

Group Art Unit: 2822

For : A PLANAR LIGHTWAVE CIRCUIT ACTIVE DEVICE METALLIZATION PROCESS

RESPONSE TO OFFICE ACTION

Assistant Commissioner for Patents & Trademarks  
Washington, D.C. 20231

Sir:

In response to the Office Action mailed on 7/18/02, please consider the following remarks:

IN THE CLAIMS

Please amend the claims as follows:

1. (Amended) A method for making a resistive heater for a planar lightwave circuit, the method comprising the steps of:
- depositing a resistive layer on a top clad of a planar lightwave circuit;
  - depositing an interconnect layer onto the resistive layer;
  - etching the interconnect layer to define a heater interconnect, wherein the heater interconnect is disposed over the resistive layer and has a first width;
  - masking the heater interconnect; and

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